

Figure 1 Packet rate vs. packet size

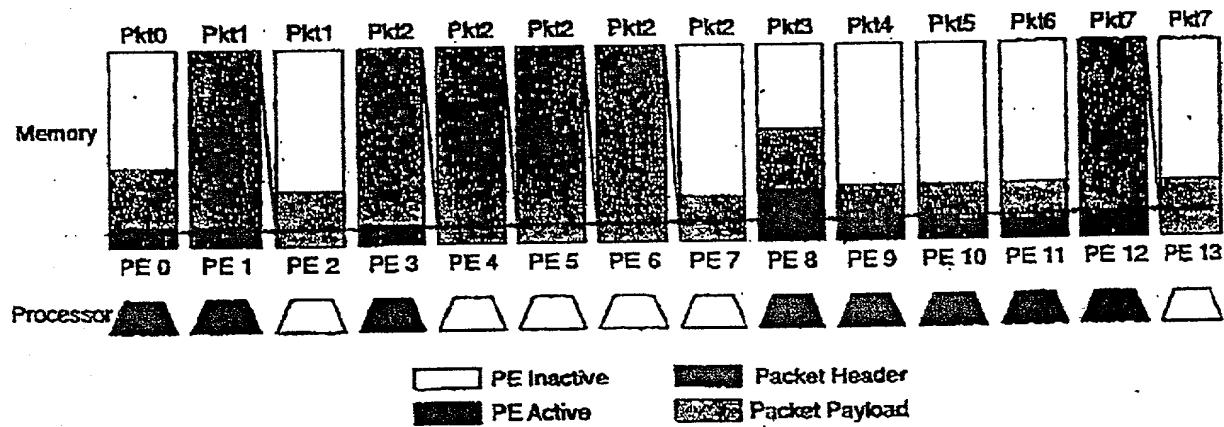


Figure 2 Allocation of packets to Processing Elements

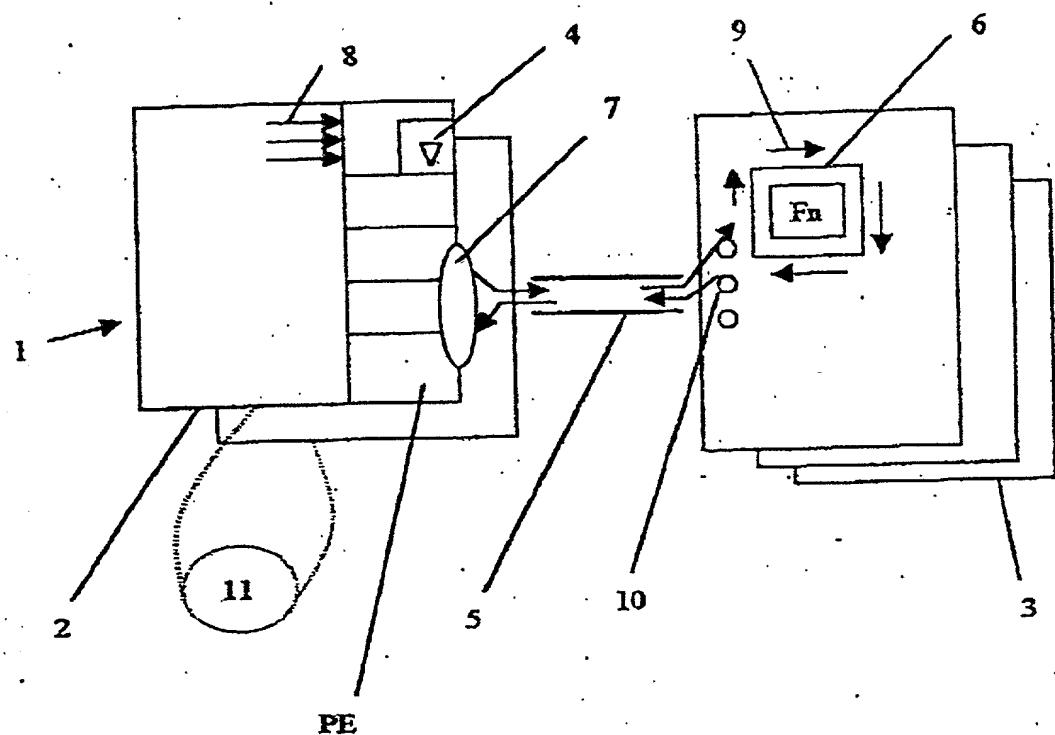


Figure 3.

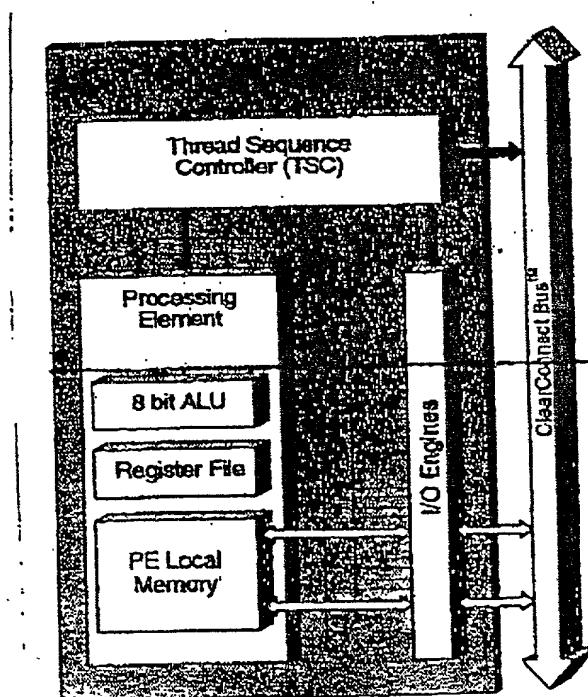


Figure 4 Structure of MTAP processor.

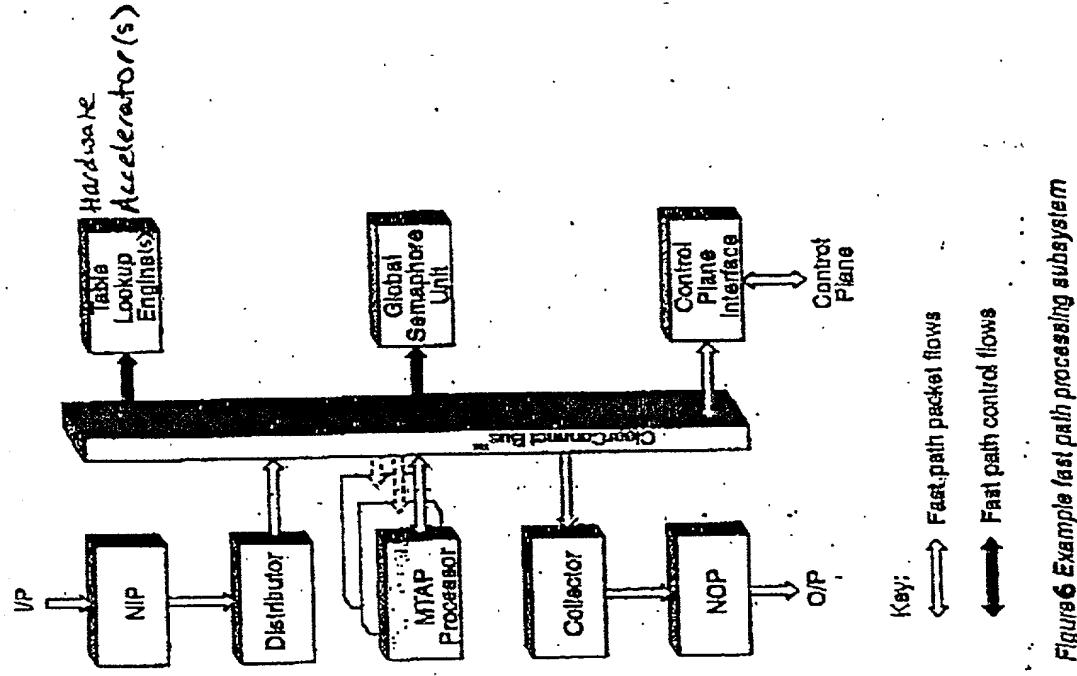


Figure 5 Example ingress pipeline

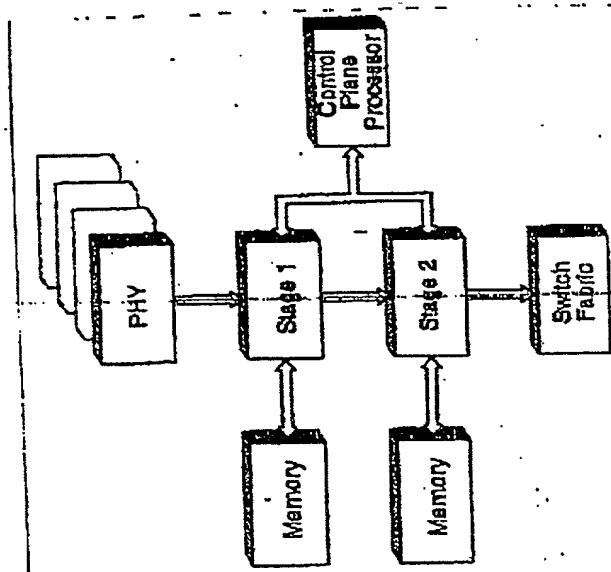


Figure 6 Example fast path processing subsystem

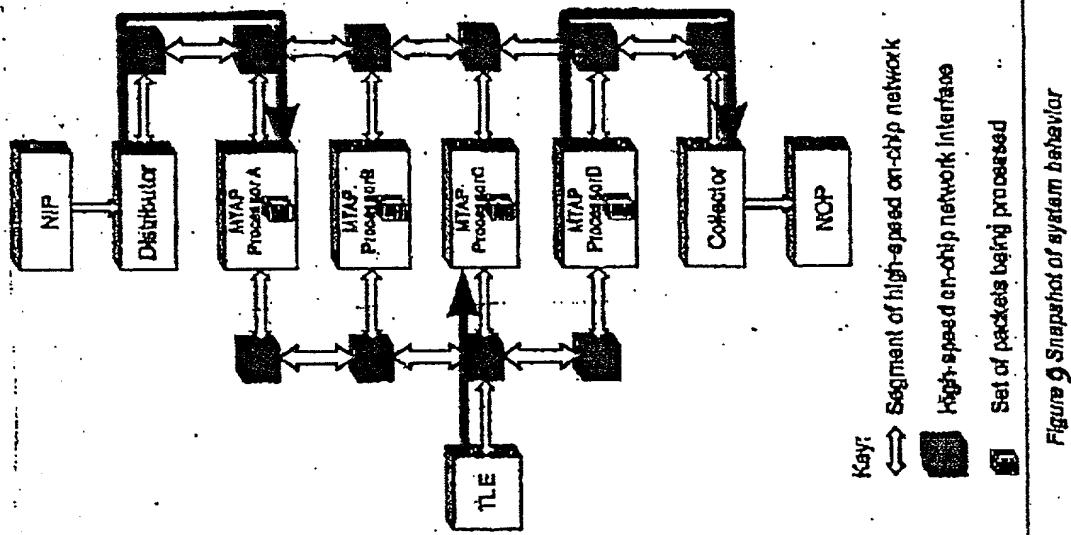


Figure 9 Snapshot of system behavior

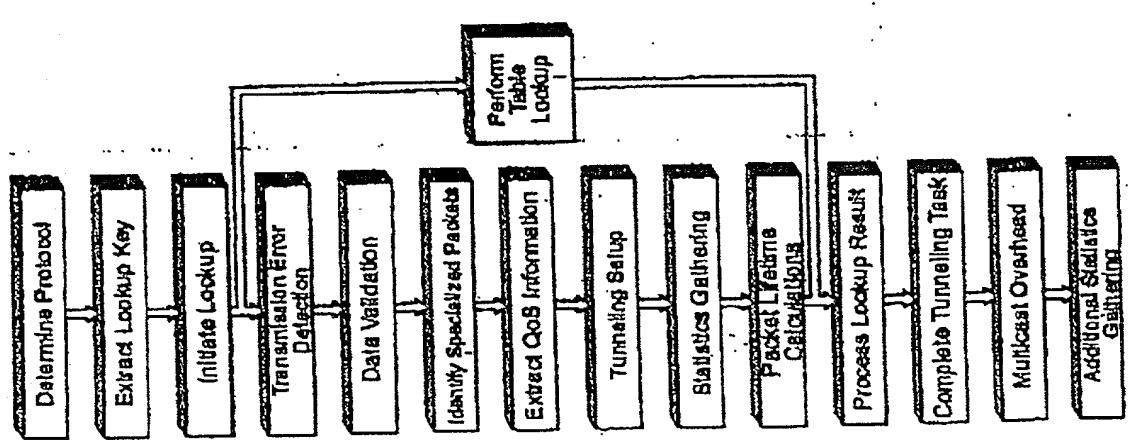
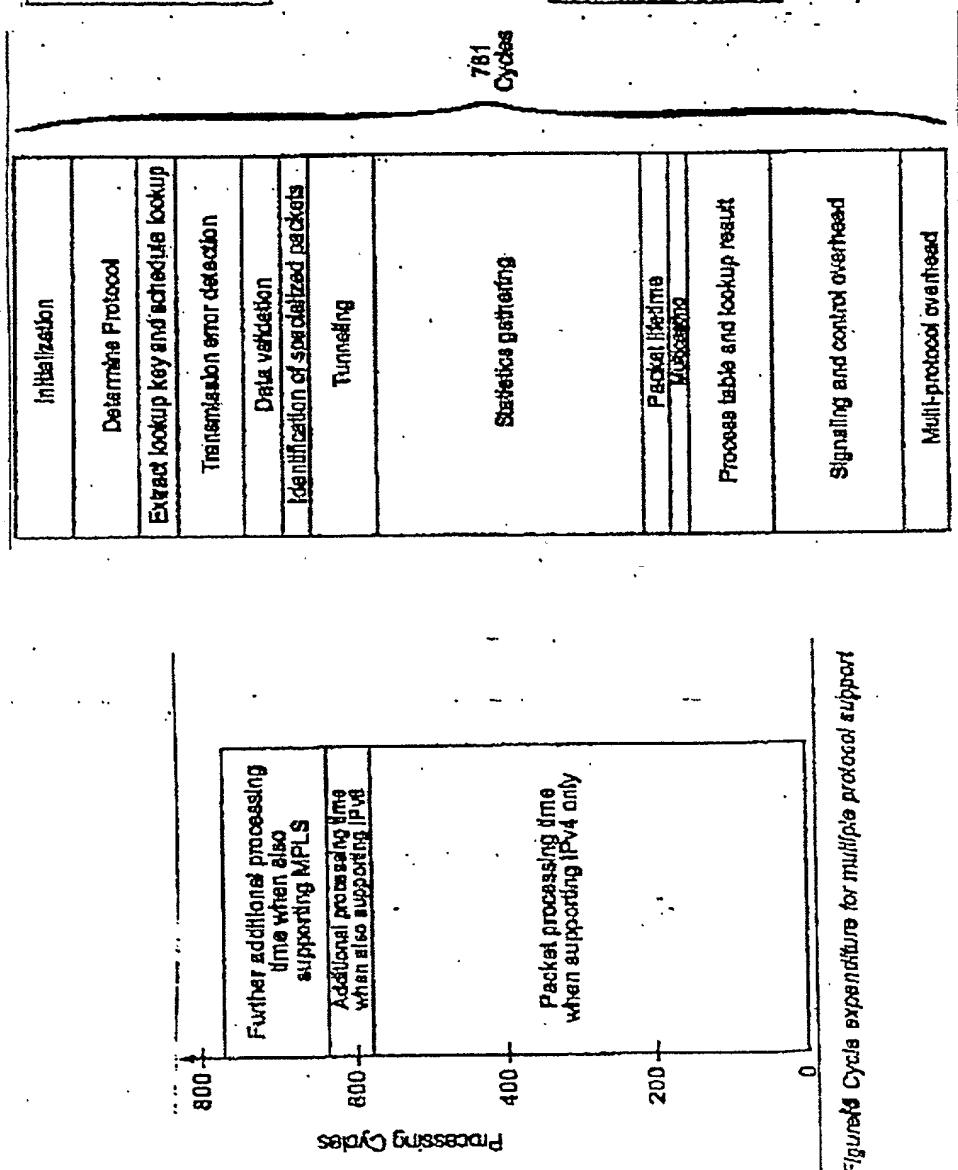


Figure 8 Overlap of packet processing and table lookup



**Figure 3** Cycle experiments for multiple protocol support

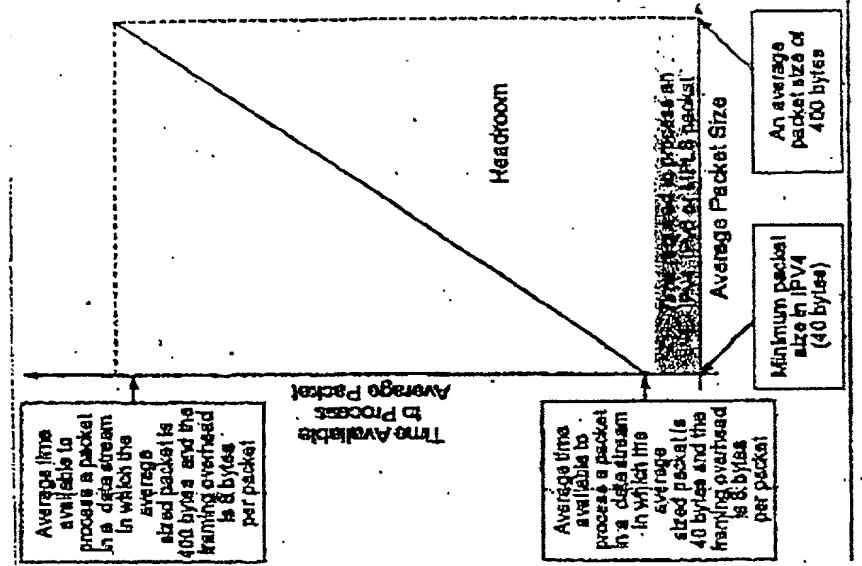


Figure 12 Headroom as a function of average packet size

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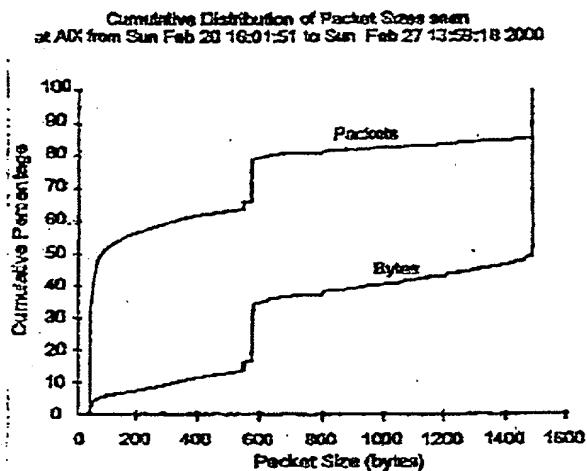


Figure 7 A real world traffic profile.

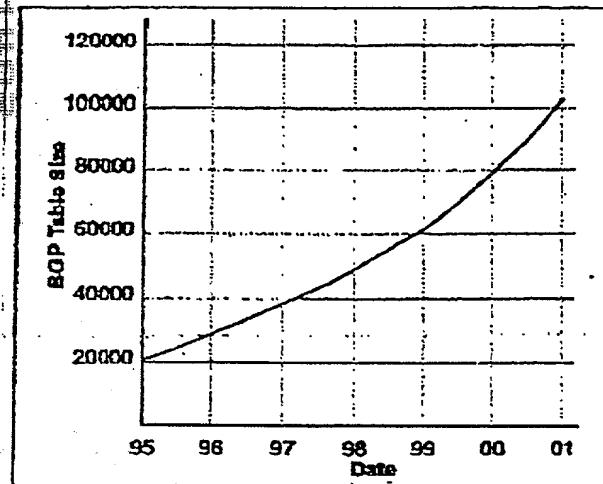


Figure 8 BGP Table Size

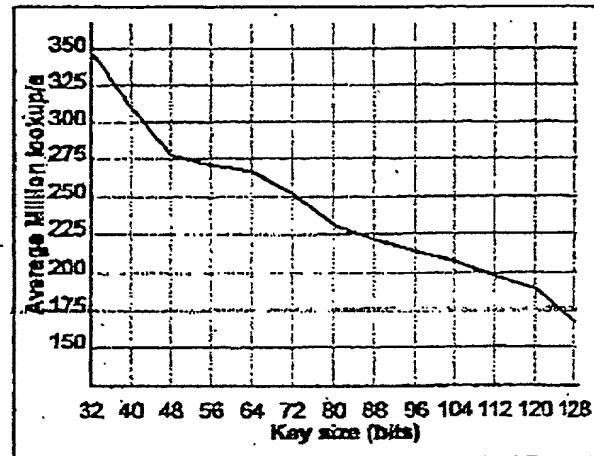


Figure 9 TLE Performance

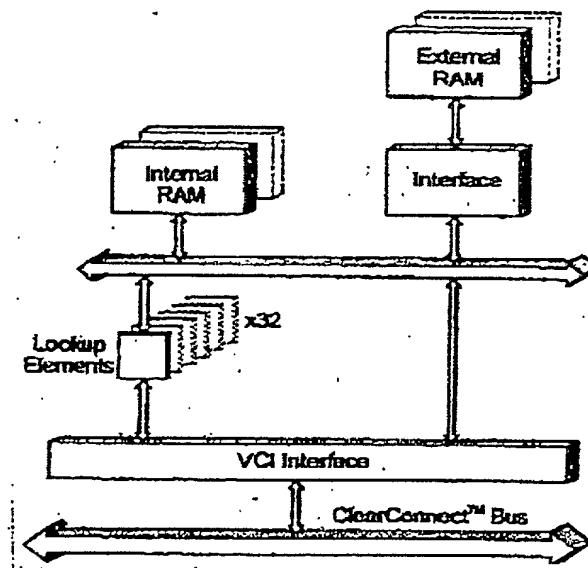


Figure 10 An example TLE